

FIGURE 1

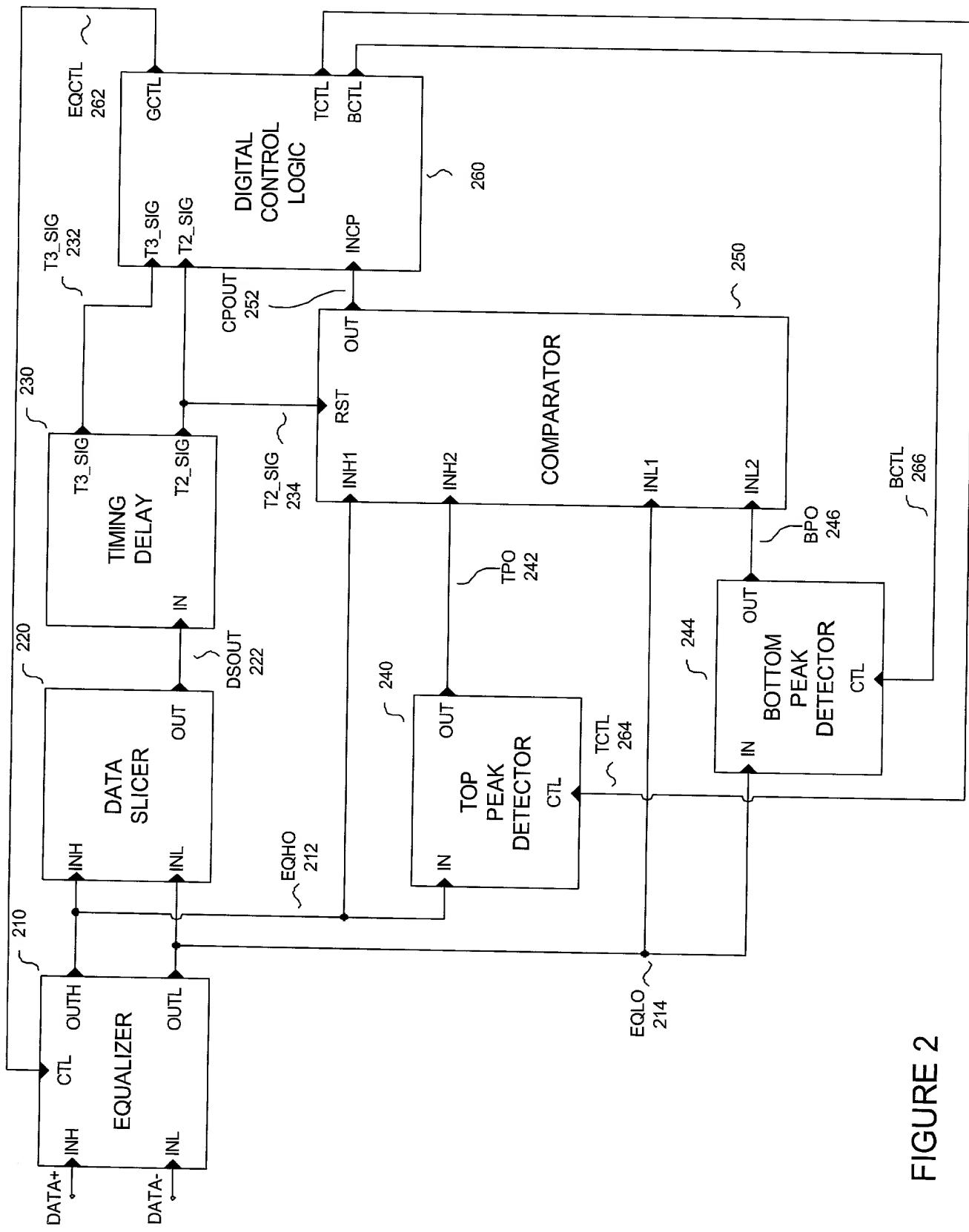


FIGURE 2

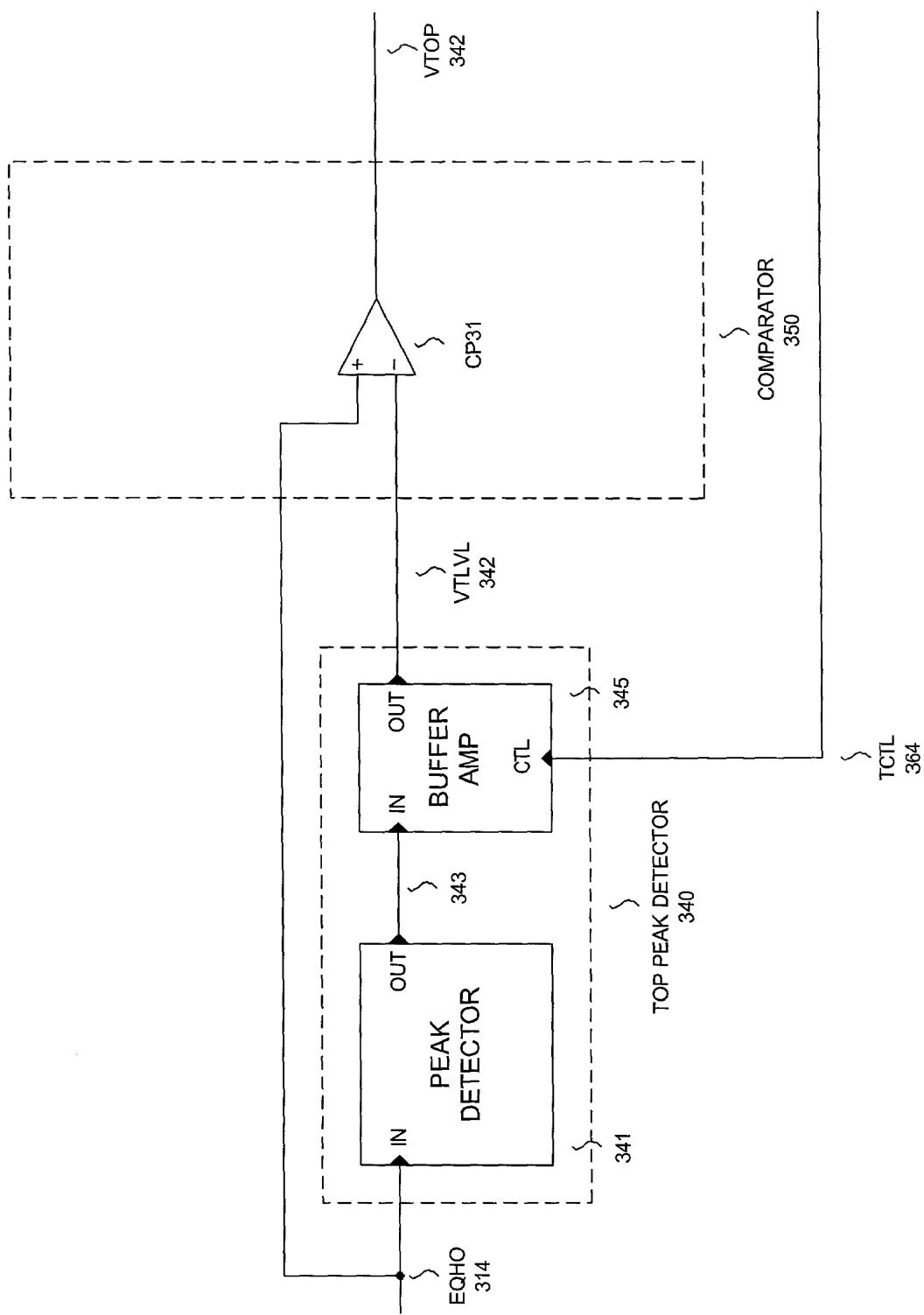


FIGURE 3

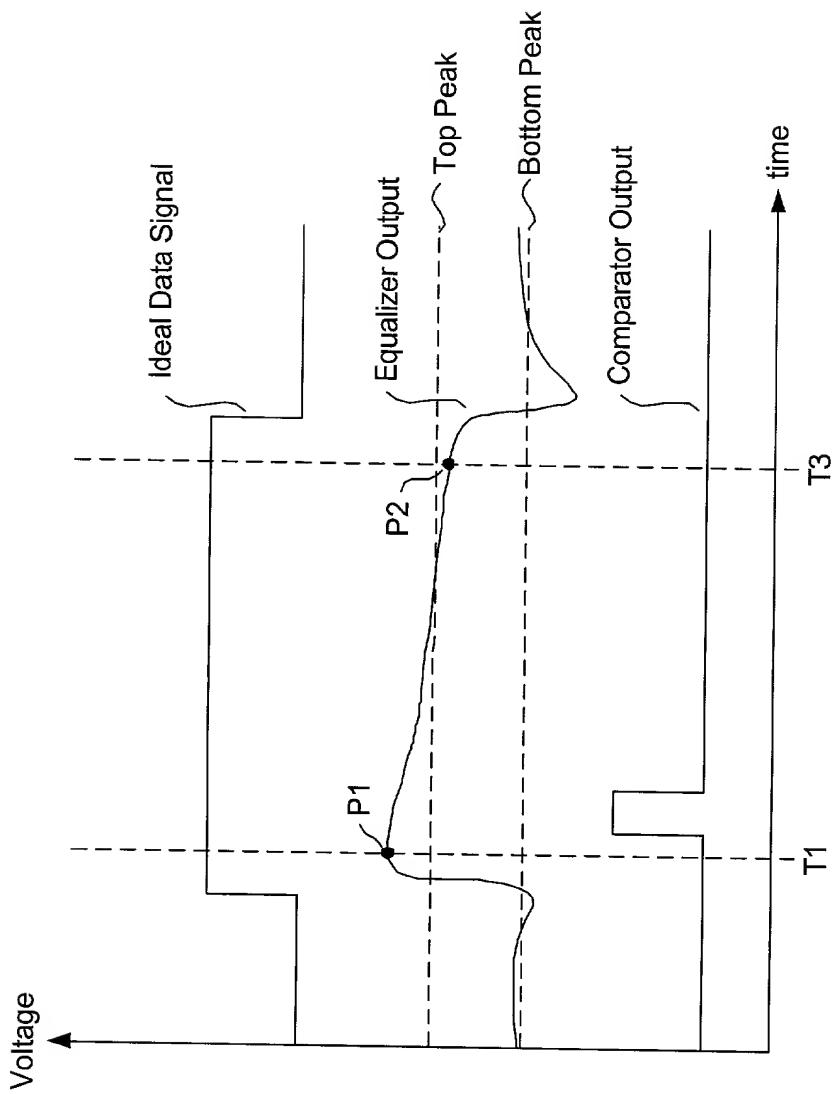


FIGURE 4

...open ...open ...open ...open ...open ...open ...open ...open ...open
...open ...open ...open ...open ...open ...open ...open ...open ...open

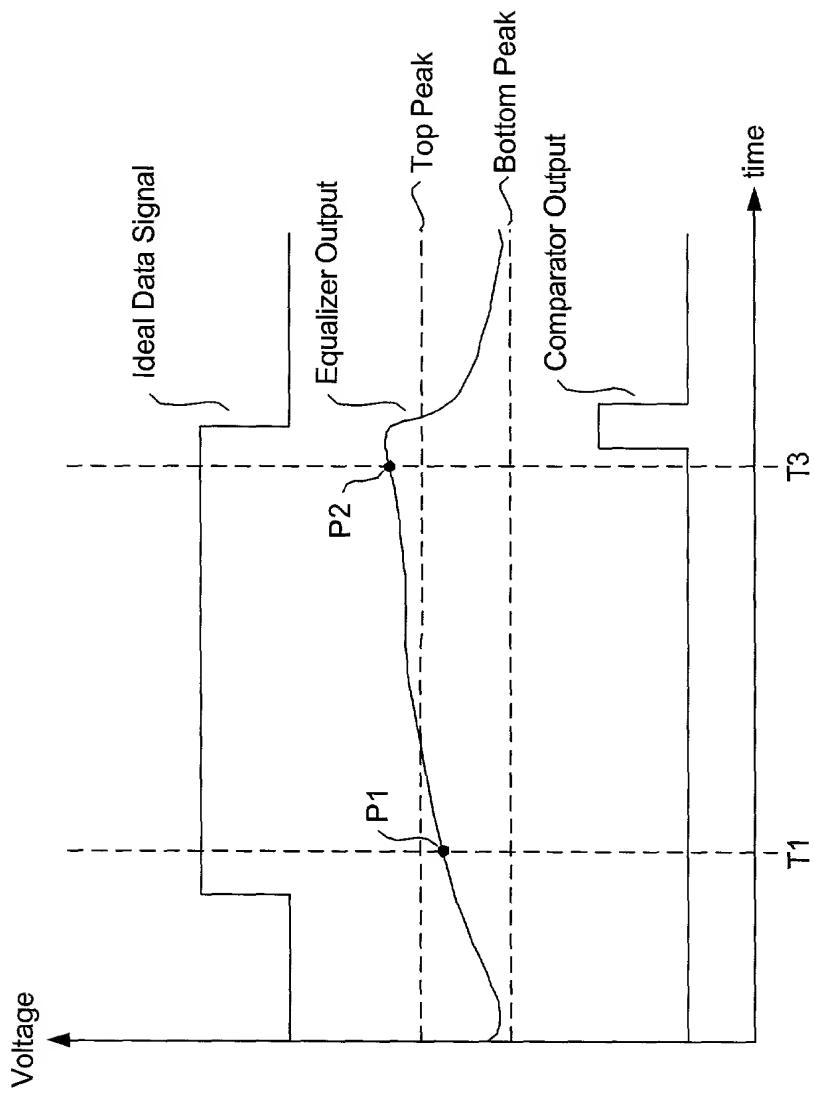


FIGURE 5

Figure 6 shows the timing diagram for the equalizer output and the comparator output.

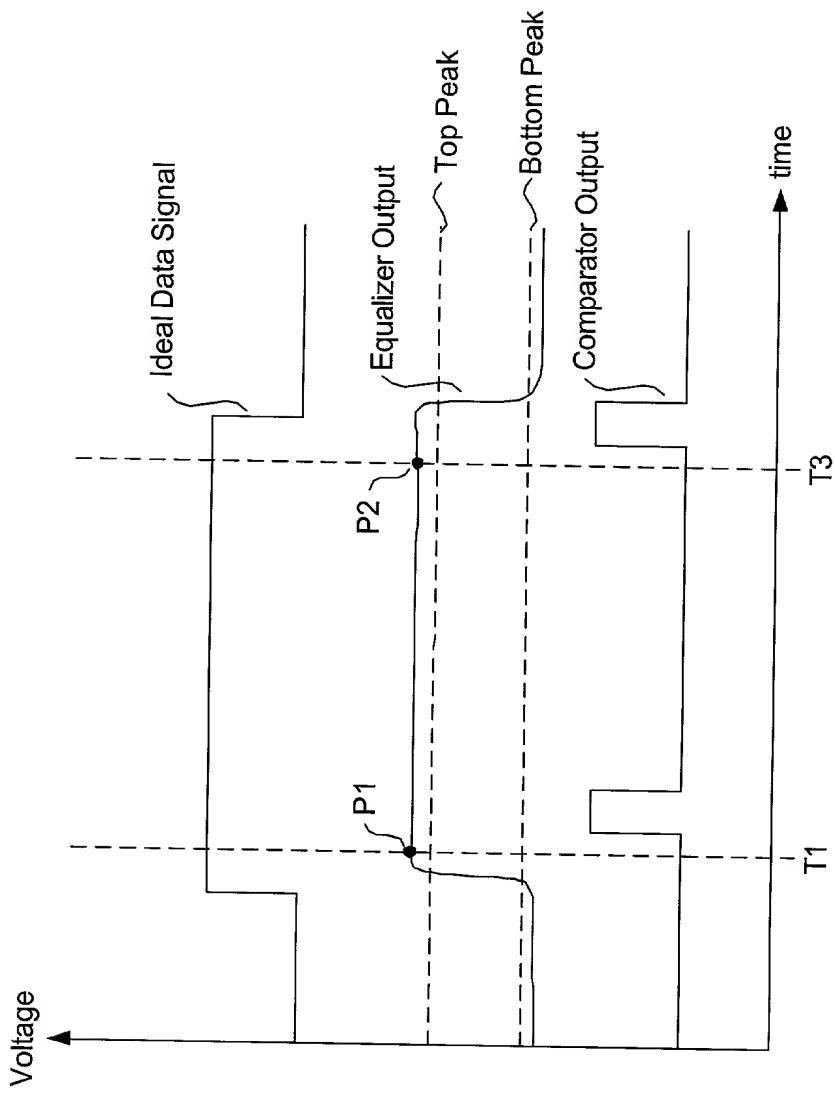
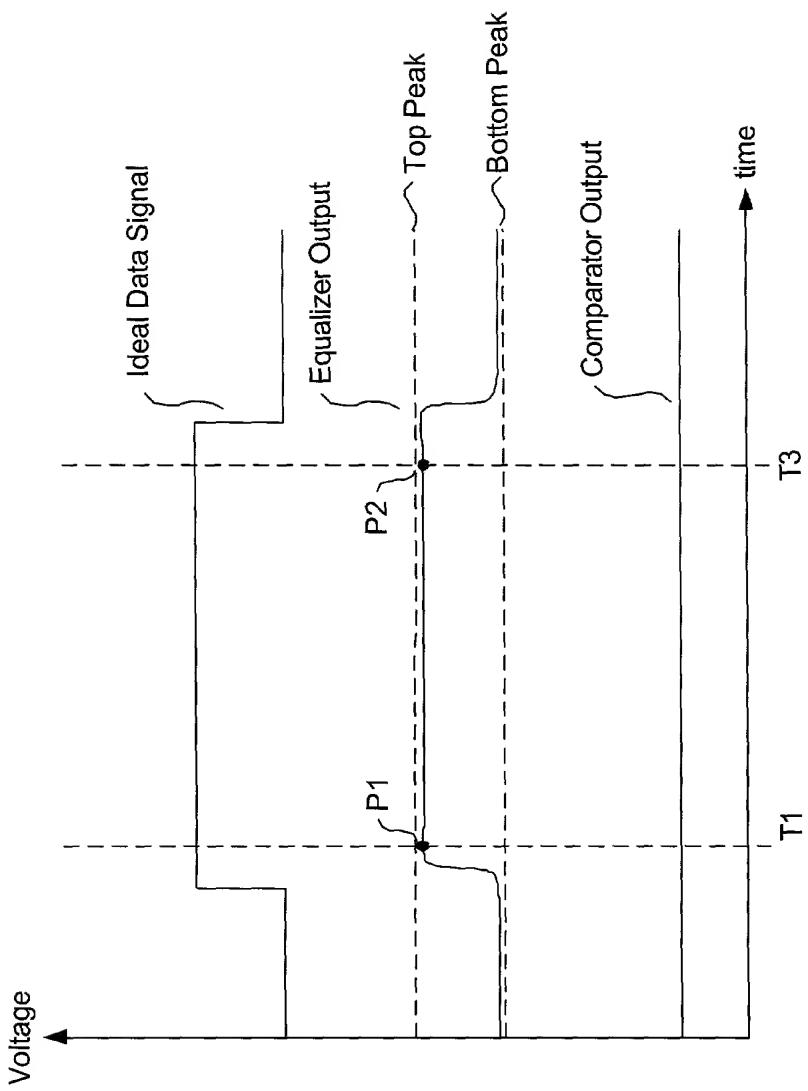


FIGURE 6

FIGURE 7



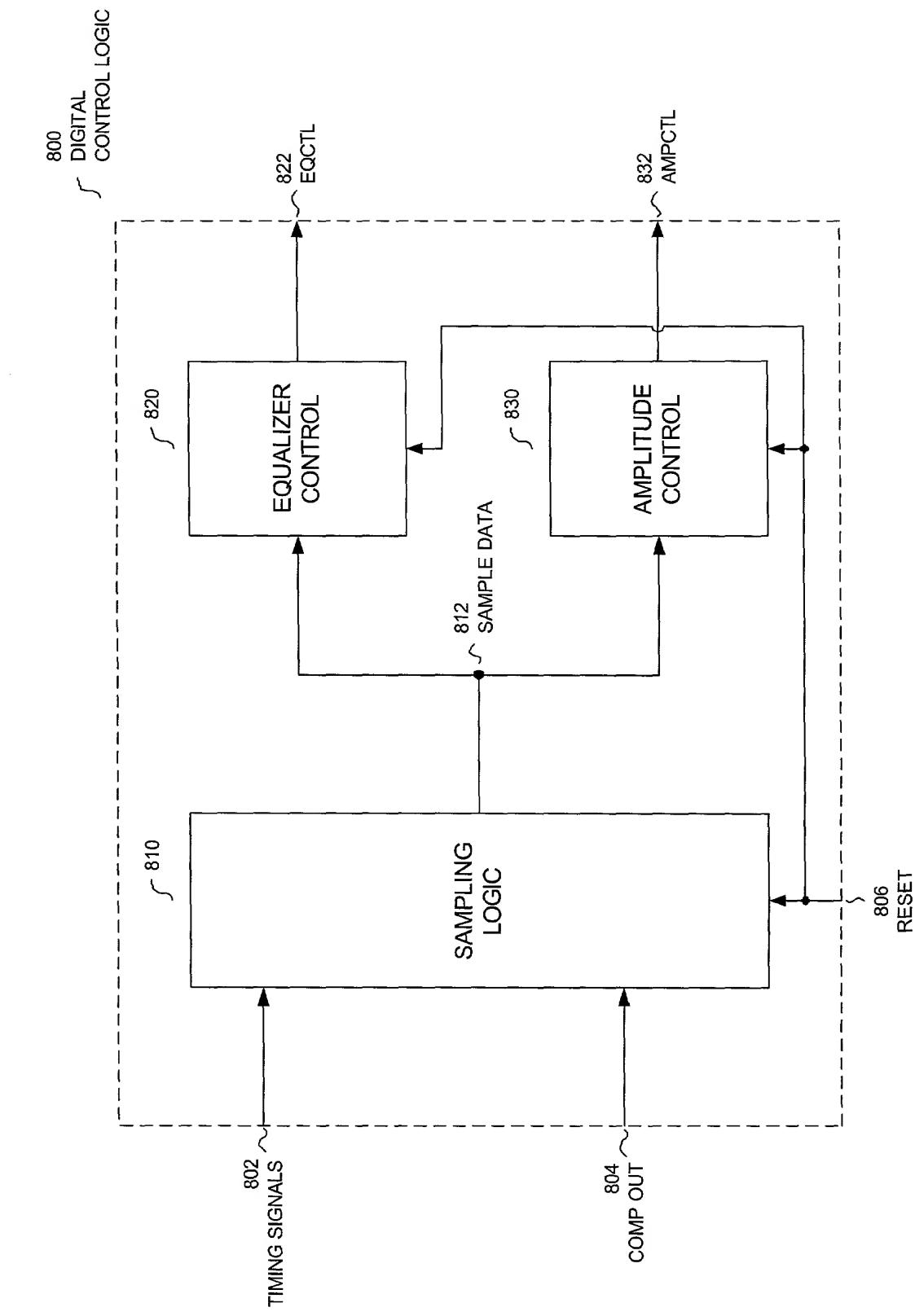
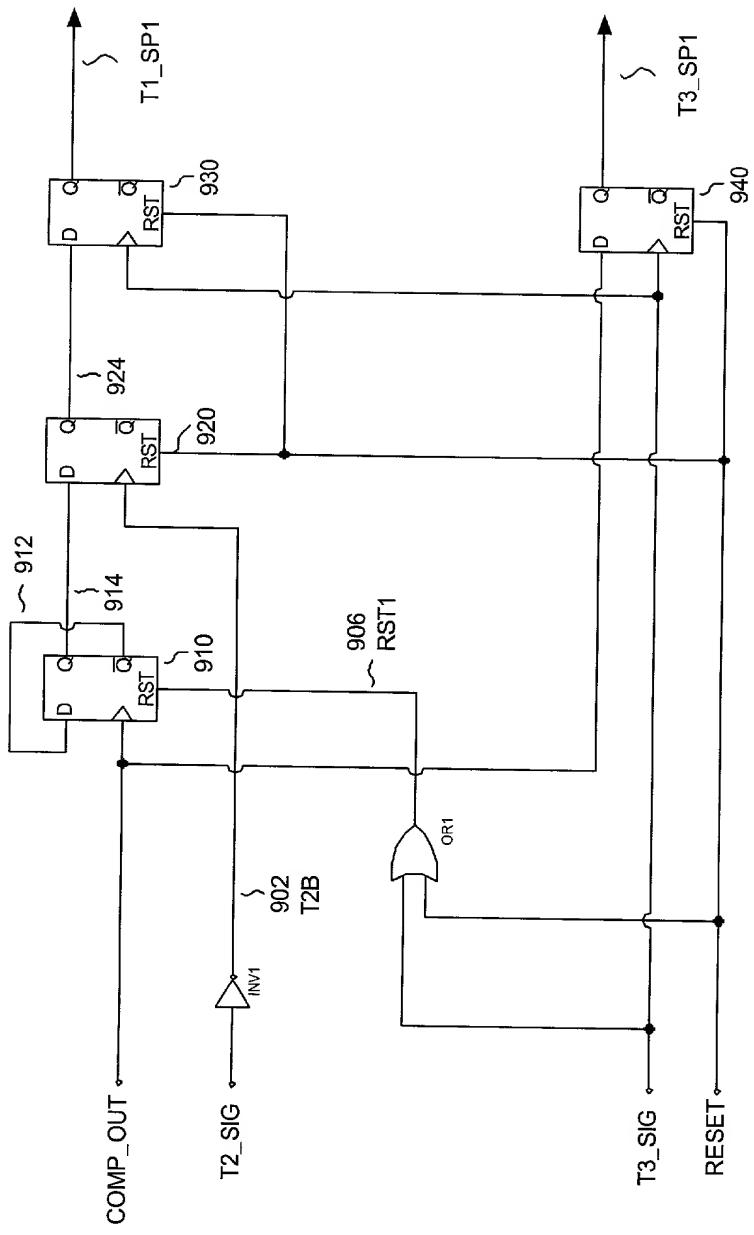


FIGURE 8

FIGURE 9



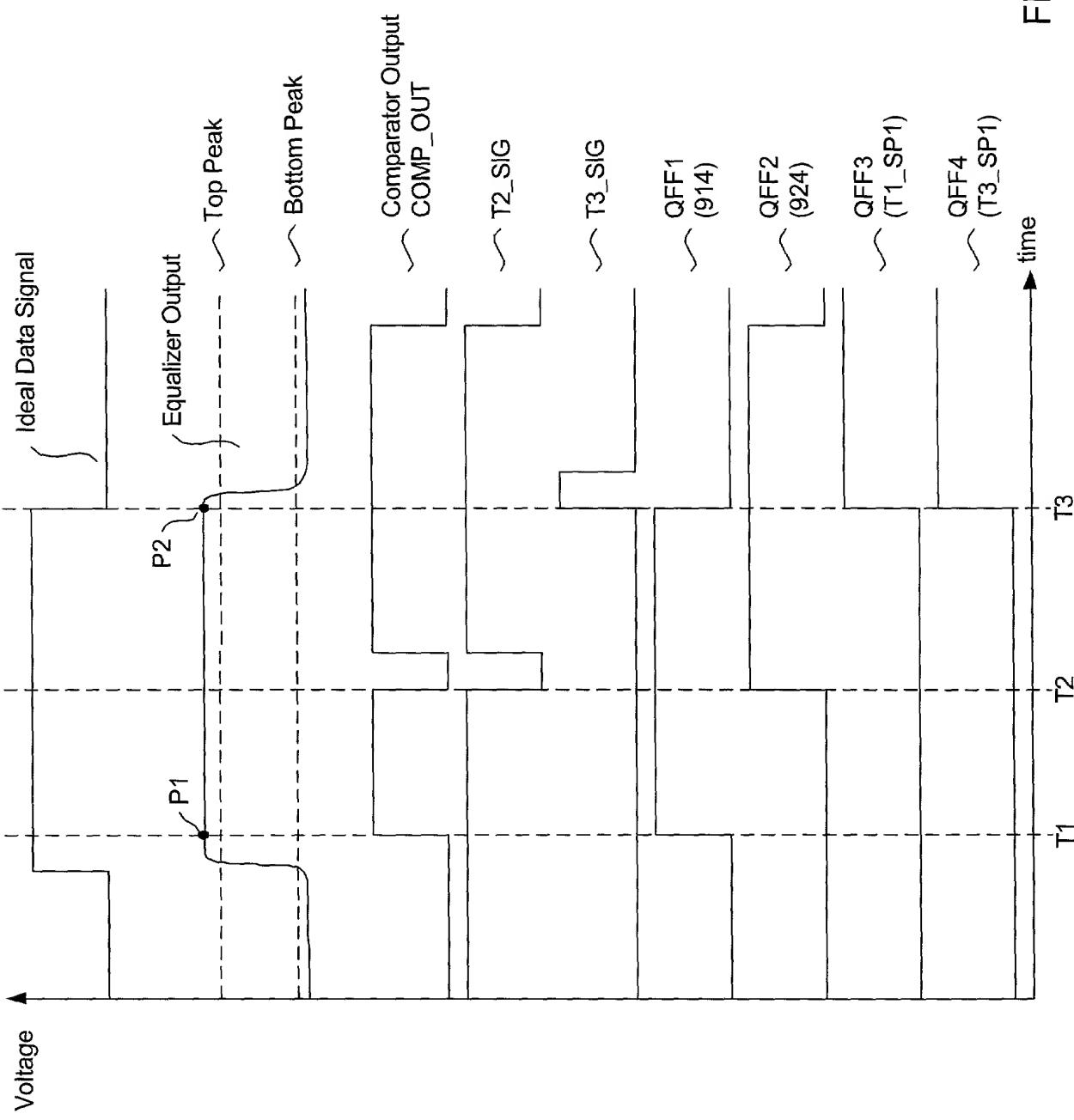


FIGURE 10

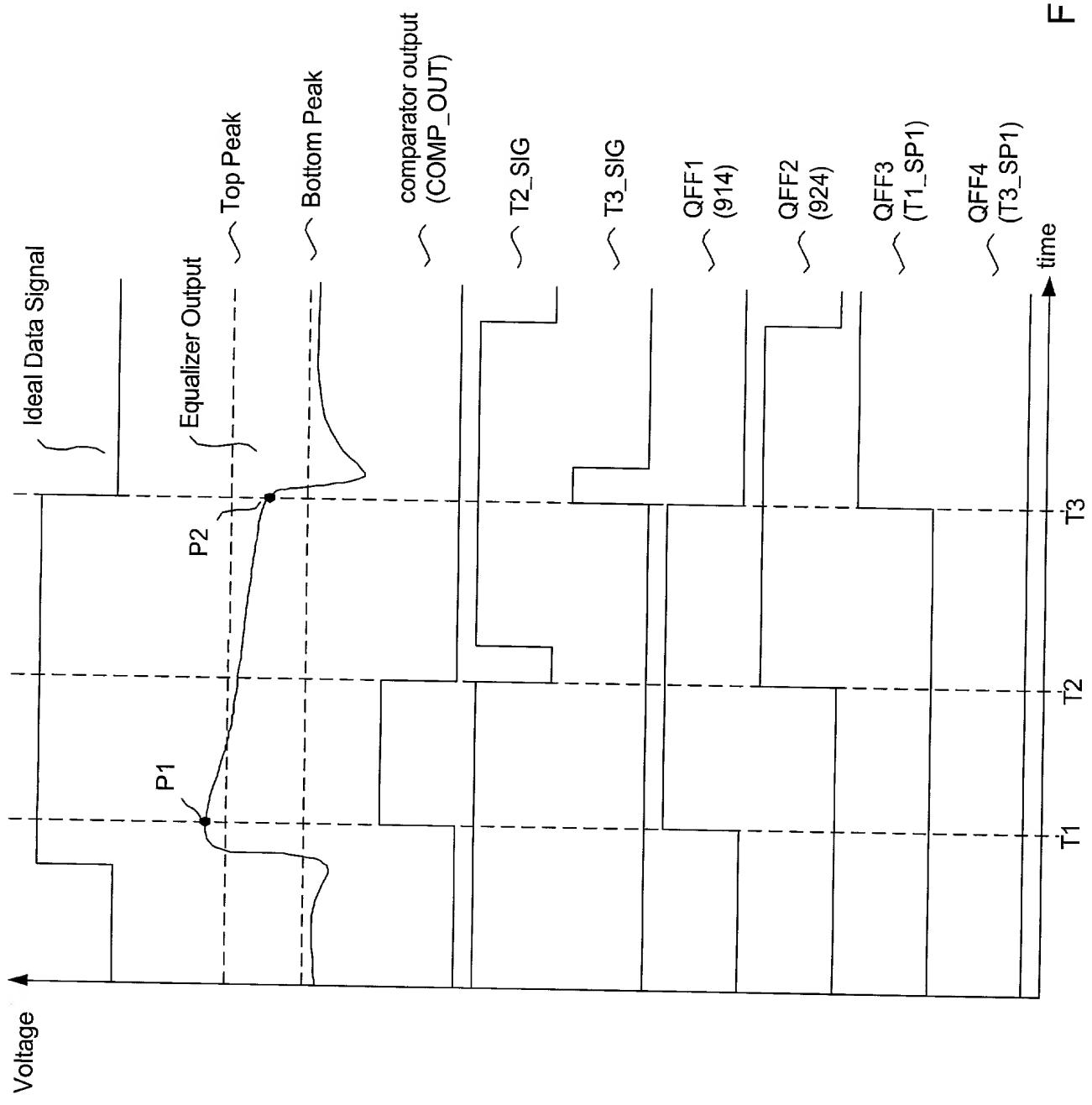


FIGURE 11

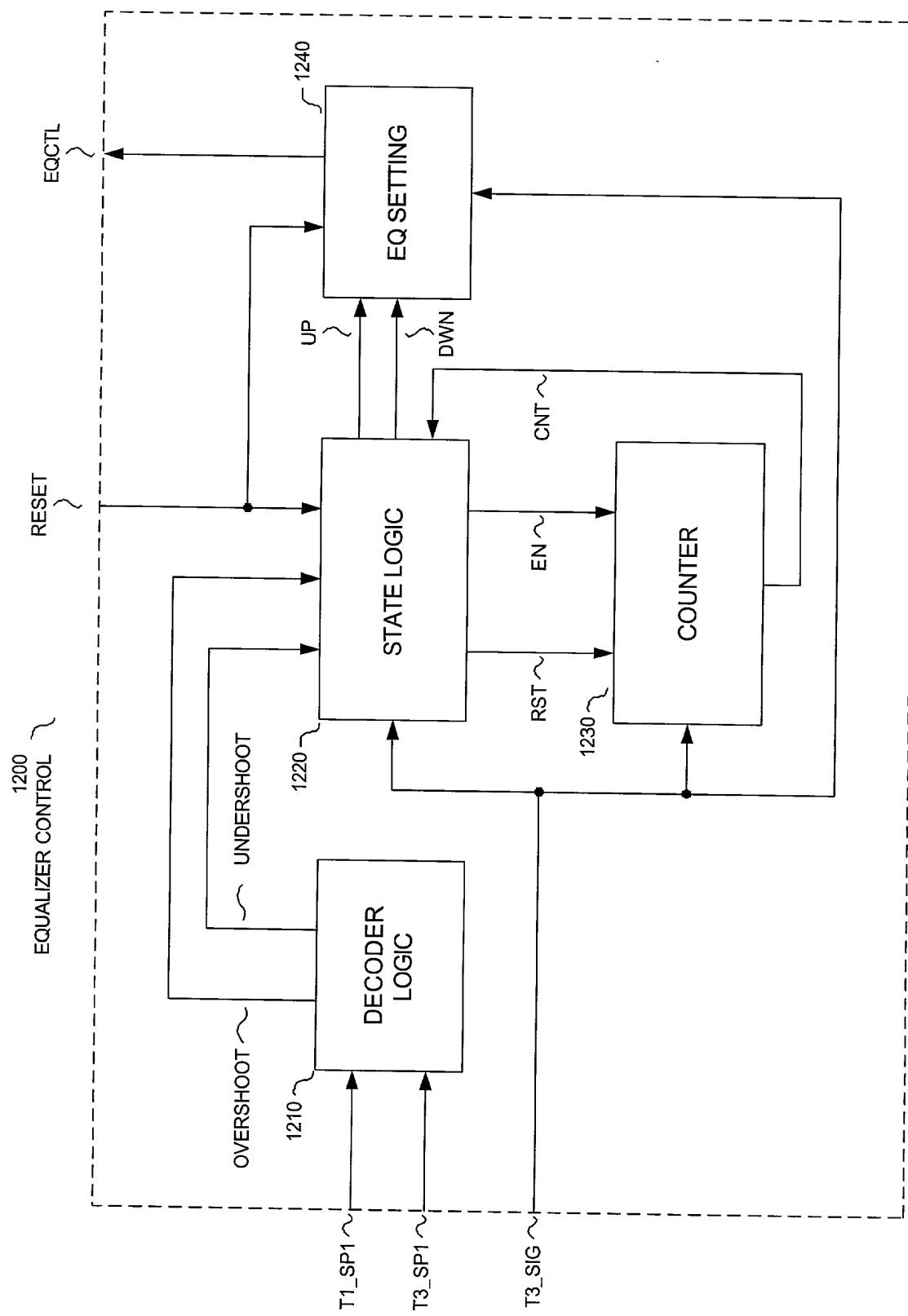


FIGURE 12

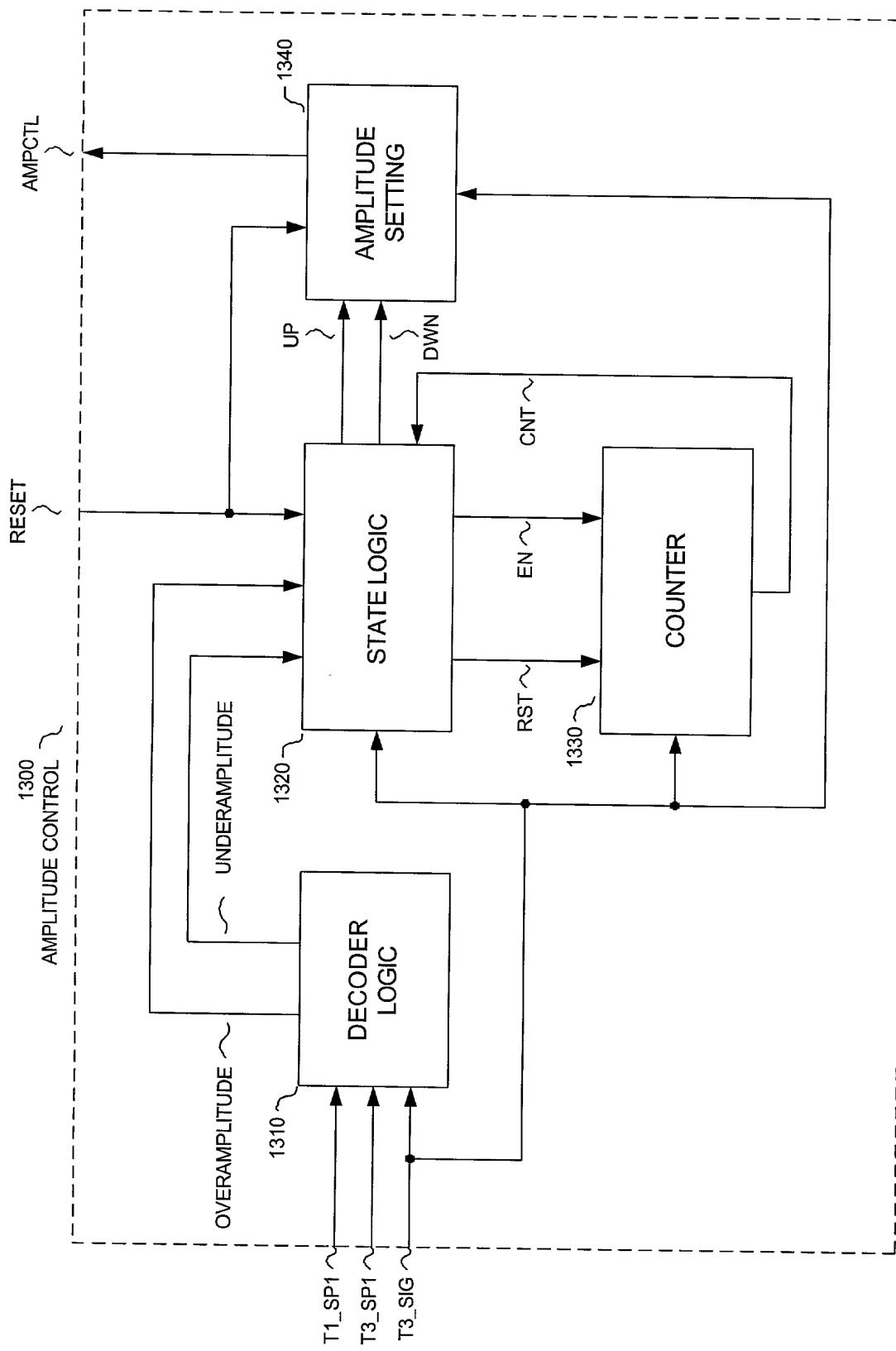


FIGURE 13